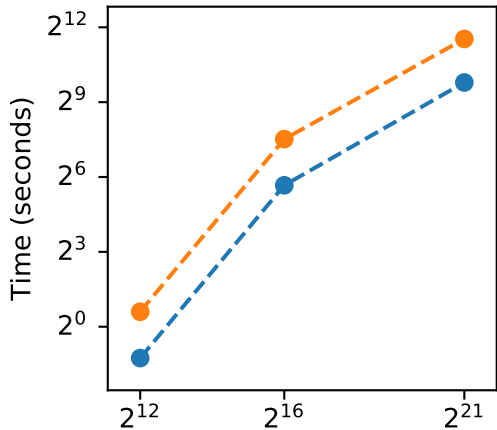
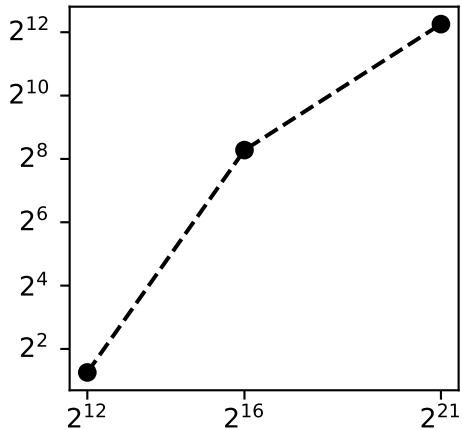


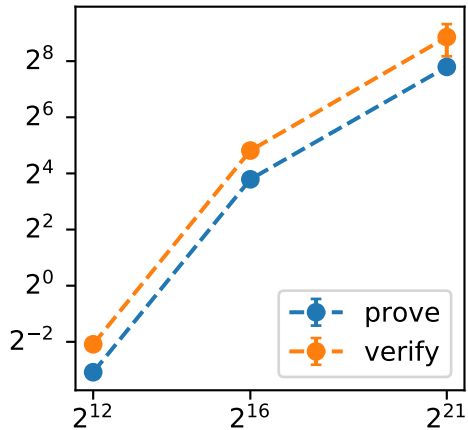
Phase 1



Intermediate FFT



Phase 2



Circuit size (gates)