



IGN5	1	PE2	132	OUT_IO9
IGN4	2	PE3	131	OUT_IO10
IGN3	3	PE4	130	OUT_IO6
IGN2	4	PE5	129	OUT_IO1
	5	PE6	128	OUT_IO5
VBAT	6	VBAT		
IGN1	7	PI8		
	8	PC13		
	9	PC14/OSC32_IN		
	10	PC15/OSC32_OUT		
	11	PI9		
	12	PI10		
	13	PI11		
VSS	14			
VDD	15			
	16	PF0		
	17	PF1		
	18	PF2		
A1	19	PF3		
A2	20	PF4		
A3	21	PF5		
VSS	22			
VDD	23			
A4	24	PF6		
A6	25	PF7		
A8	26	PF8		
A7	27	PF9		
A16	28	PF10		
X0	29	PH0/OSC_IN		
X1	30	PH1/OSC_OUT		
nReset	31	NRST		
A9	32	PC0		
A10	33	PC1		
A11	34	PC2_C		
A14	35	PC3_C		
VDD	36			
VSSA	37			
VREF+	38			
VDDA	39			
PA0	40			
A12	41			
A15	42			
A23	43			
A22	44			

PH4	45			
PH5	46			
PH3	47			
VSS / BYPASS_REG	48			
VDD	49			
PA4	50			
PA5	51			
PA6	52			
PA7	53			
PA8	54			
PA9	55			
PC4	56			
PC5	57			
PB0	58			
PB1	59			
PB2	60			
PB3	61			
PB4	62			
PB5	63			
PB6	64			
PB7	65			
PB8	66			
PB9	67			
PB10	68			
PB11	69			
PB12	70			
VSS	71			
VDD	72			
PE10	73			
PE11	74			
PE12	75			
PE13	76			
PE14	77			
PE15	78			
PG0	79			
PG1	80			
PG2	81			
PG3	82			
PG4	83			
PG5	84			
PG6	85			
PG7	86			
PG8	87			
PG9	88			
PH12	89			

\*A20-A23 are duplicated for ADC3  
 \*VIGN input is unprotected!

\*D5 is digital-only on F429!(EINT11)  
 \*A20-A22 are duplicated for COMP1/2