

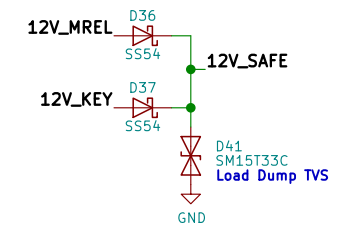
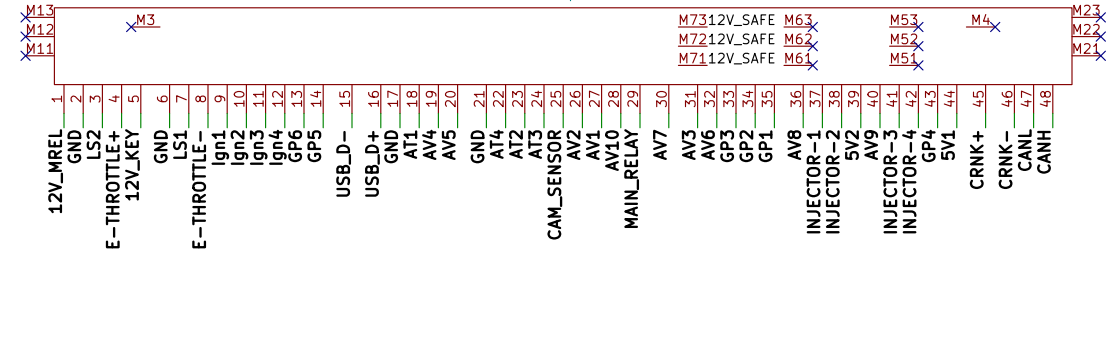
U3E
LIN1LINBUS

U3A
molex_48pin_MRE

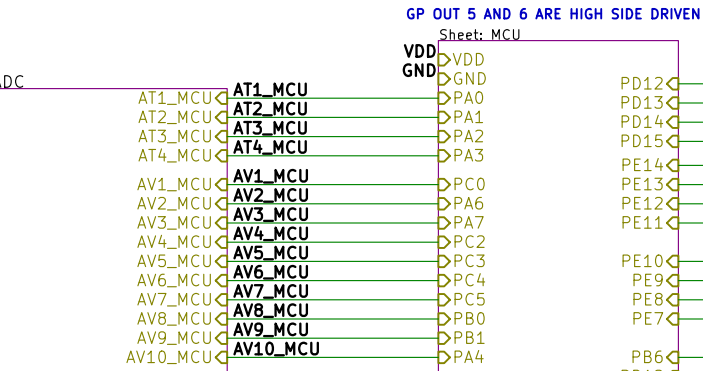
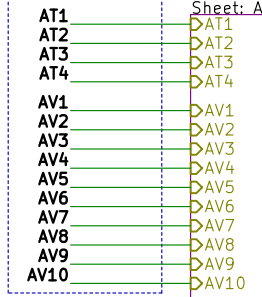
U3D
5VJ801 J80212V_MREL
PB8J803 J804VDD
PC11J805 J806PB9
PA15J807 J808PC10
GNDJ809 J810PC12

Communication Header
J4
Conn_02x05_Counter_Clockwise
5V 1 2 12V_MREL
PB8 3 4 VDD
PC11 5 6 PB9
PA15 7 8 PC10
GND 9 10 PC12

5V TP5
GND TP0
CAM_MCU TP2
CRANK TP4
12V_MREL TP12
VDD TP3

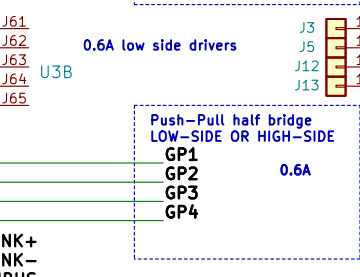
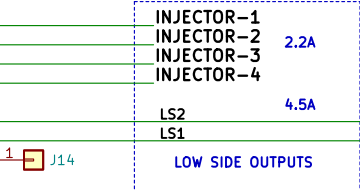
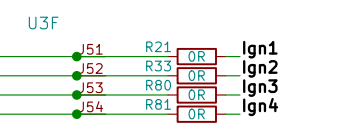
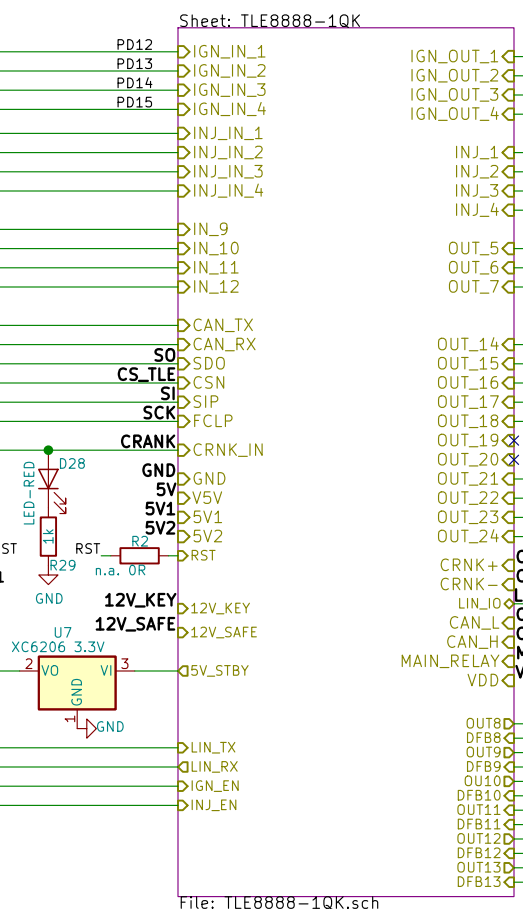
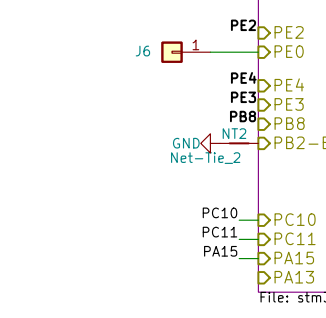
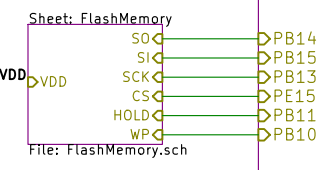
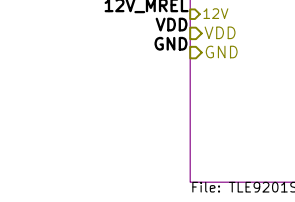


ANALOG INPUTS.
ADC 1-4 HAVE
BIAS RESISTORS
FOR TEMP SENSORS.
CAN ALSO BE USED
AS DIGITAL INPUTS

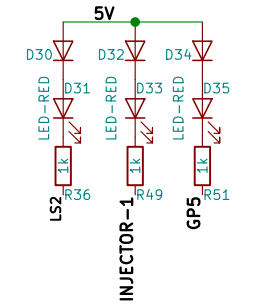


GP OUT 5 AND 6 ARE HIGH SIDE DRIVEN

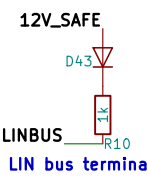
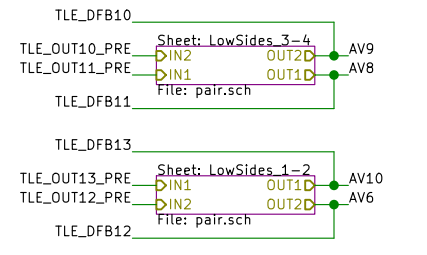
6A H-BRIDGE
E-THRITTLE+
E-THRITTLE-



Populate for freewheeling.
Bypasses internal clamps.



LED-YELLOW PE1
LED-BLUE PE2
LED-RED PE3
LED-GREEN PE4
PE1 is yellow - warning
PE2 is blue - communication
PE3 is red - fatal
PE4 is green - running



A160D
Donald Becker

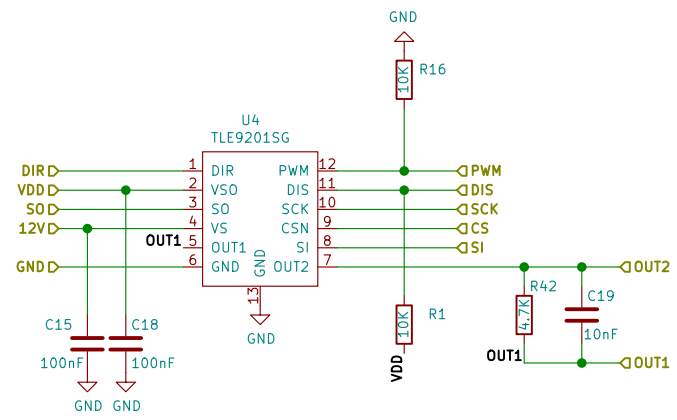
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KiCad E.D.A. kicad (5.1.5)-3

Rev: R0.5.0
Id: 1/9





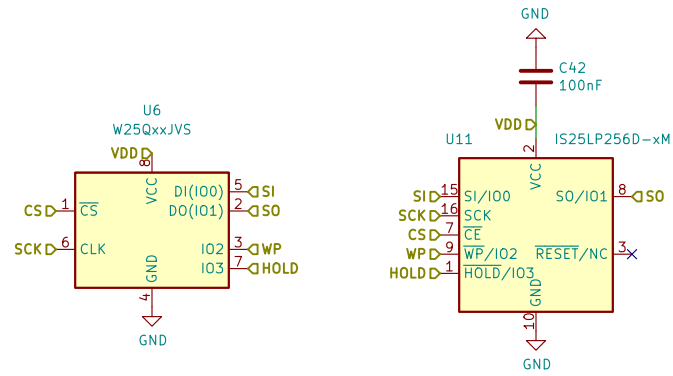
AI60D
 Donald Becker

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Rev: R0.5.0
 Id: 2/9



AI60D
Donald Becker

rusEFI.com

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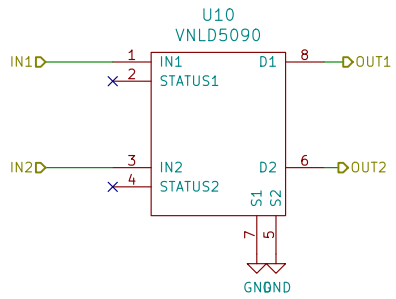
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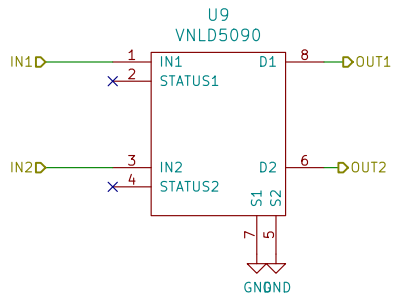
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Rev: R0.5.0

Id: 3/9

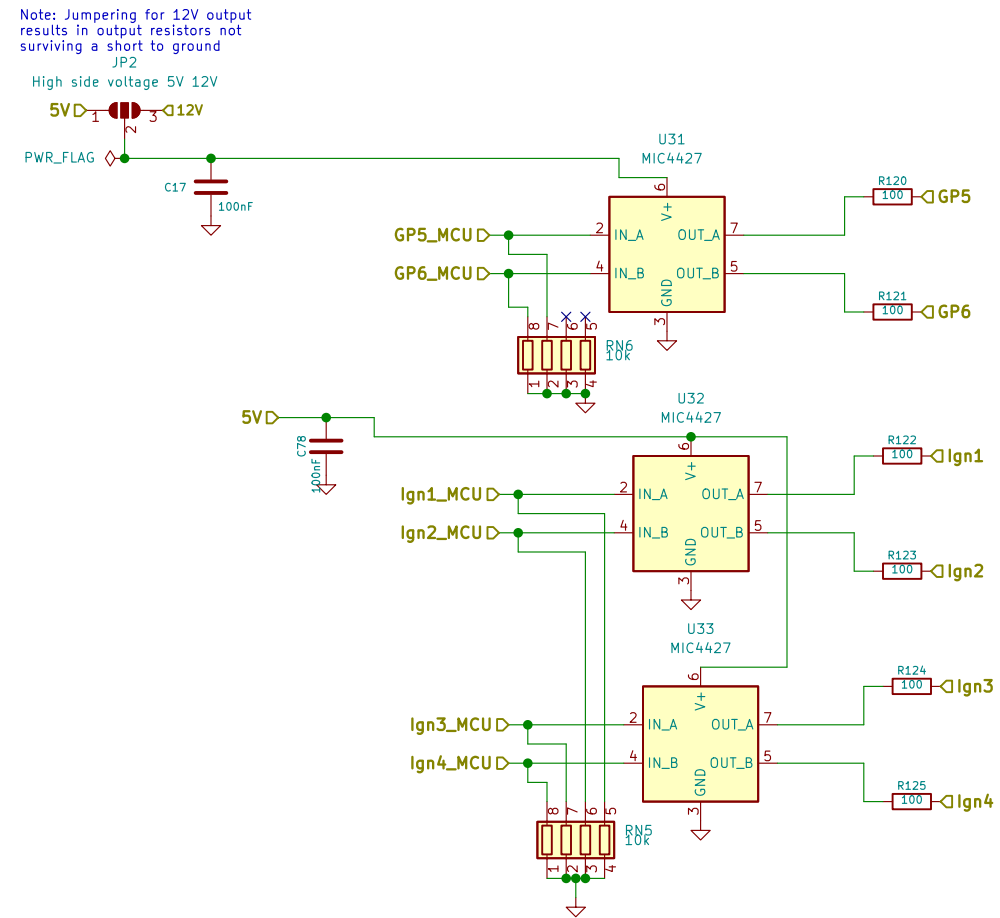


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	Id: 4/9



rusEfi.com		
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Size: A4	Date: 2020-04-22	Rev: R0.5.0
KiCad E.D.A. kicad (5.1.5)-3		Id: 5/9

6 channel high / low side driver

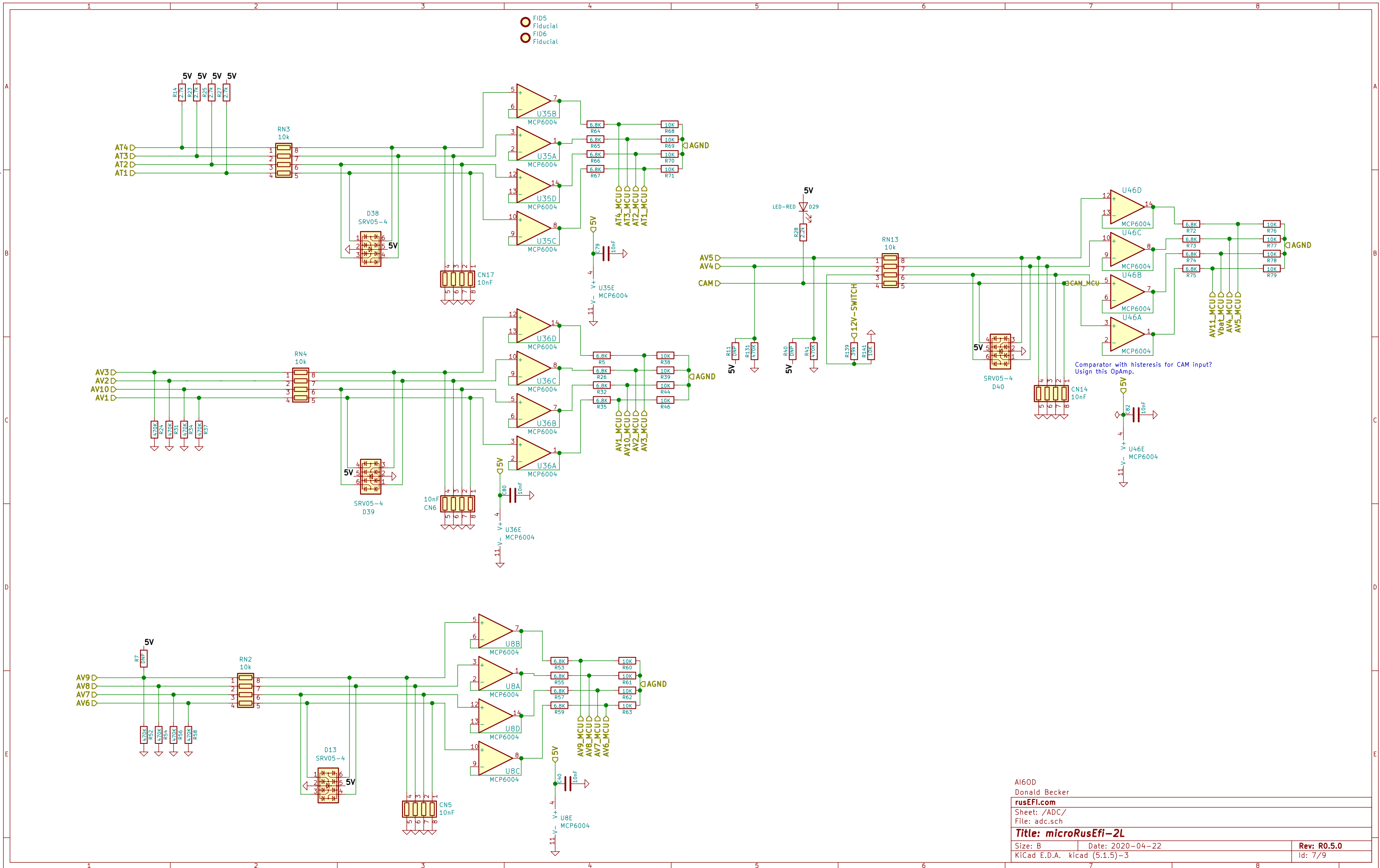


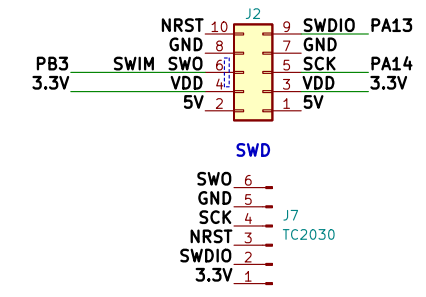
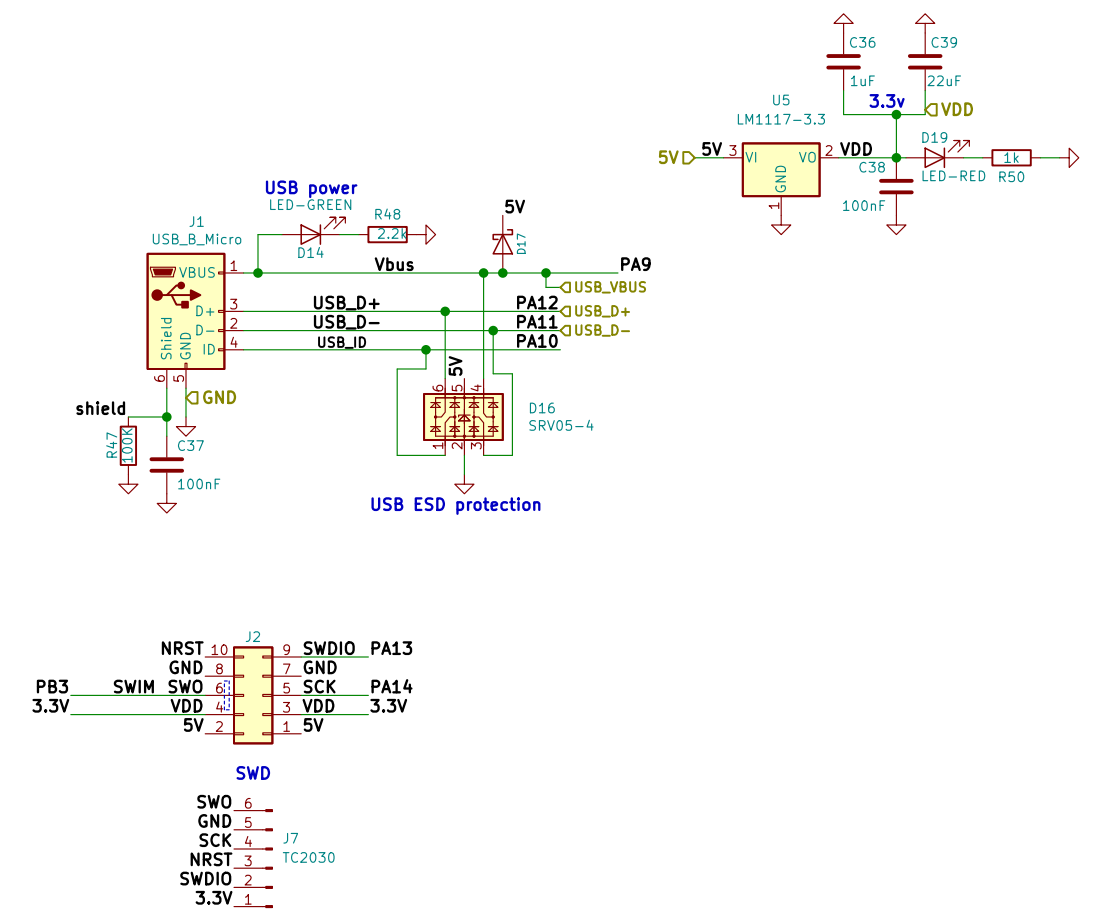
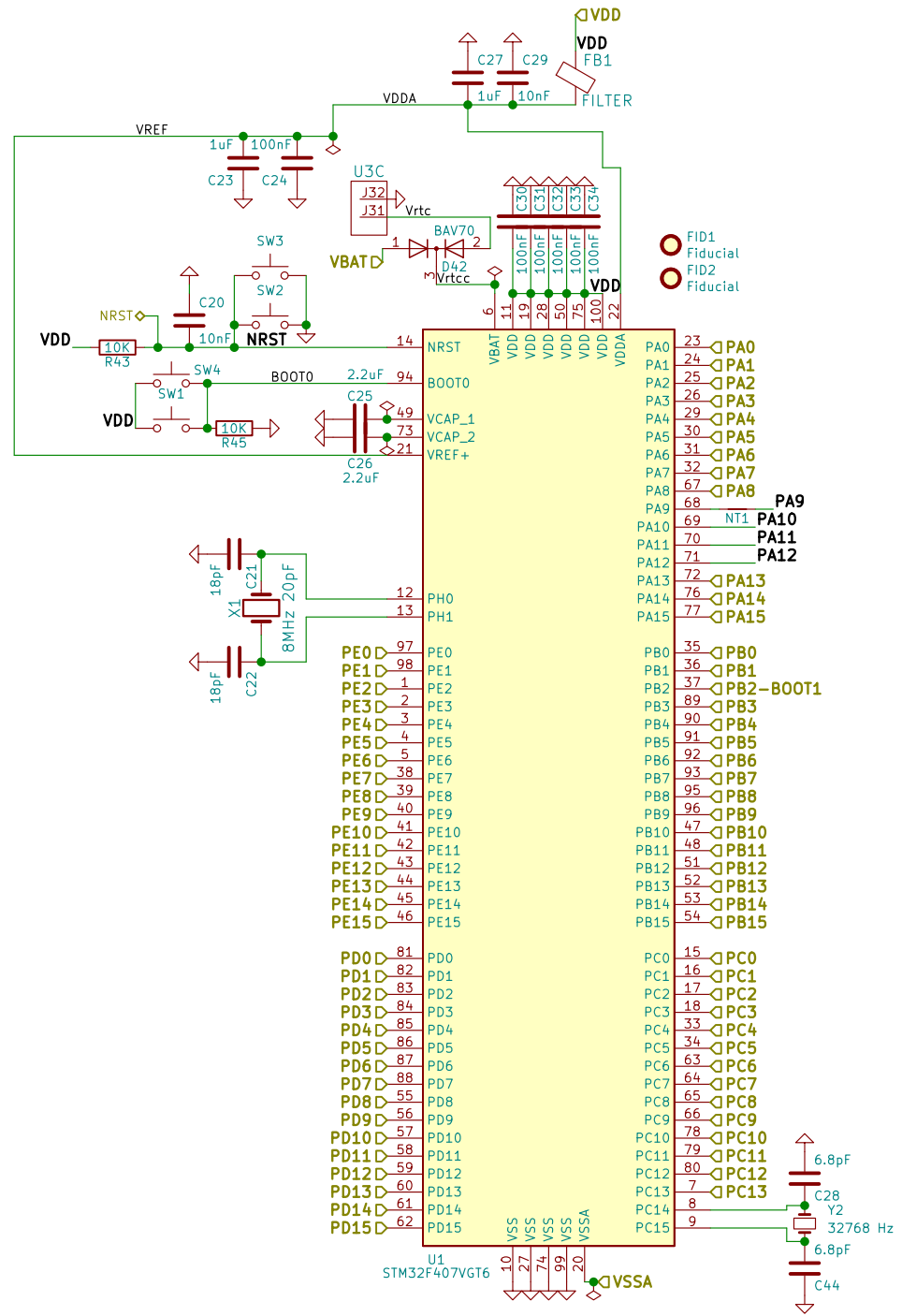
AI60D
Donald Becker
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KiCad E.D.A. kicad (5.1.5)-3

Rev: R0.5.0
Id: 6/9





<http://www.crystek.com/documents/appnotes/Pierce-GateIntroduction.pdf>
 PCB per predictions with SaturnPCB has less than 3.5pF traces.
 STM32 pins assumed 5pF
 ESR = 80ohms max???
 Rf = 2meg could be between 1meg and 10meg.
 Cload should be 8pF per XTAL datasheet
 Cload = ((Cin+C1)[C2+Cout])/(Cin+C1+C2.Cout)+PCBstray
 Cload = (([5+4.7][4.7+5])/([5+4.7+4.7+5])+3.5= 8.35pF
 C1=C2=C166=C167 = 4.7pF
 Rs = 1/(2pi*fC2) = 1/(2*pi*8MHz*4.7pF) = 4.2ohms.